

The diagram illustrates a 16-channel multiplexer system. It features two TL064 multiplexers, each with 16 channels. The multiplexers are connected to 16 input channels (J_1 to J_16) and 16 output channels (J_1 to J_16). Each input channel is connected to a 100k resistor and a 10uF capacitor. Each output channel is connected to a 240 resistor and a 10uF capacitor. The multiplexers are also connected to a 74VHC125 buffer, which is used to drive the output channels. The buffer is connected to the output channels via 75k resistors. The buffer is also connected to the input channels via 75k resistors. The buffer is powered by a 5V supply and a 10uF capacitor.